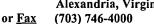
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(Depositor's name)	John Stattler
(Signature)	In shuth
(Date)	2/11/2004

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/854,146	05/11/2001	Jun Li	SPLX.P0050	6790

TITLE OF INVENTION: HIGH ACCURACY TIMING MODEL FOR INTEGRATED CIRCUIT VERIFICATION

APPLN. TYPE	SMALL ENTITY	ISSUE F	EE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	0	\$300	\$1630	02/17/2004
EXAMINER		ART UN	IIT	CLASS-SUBCLASS	7	
SIEK, VUTHE		2825		716-006000	_	
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